

FIG 1D

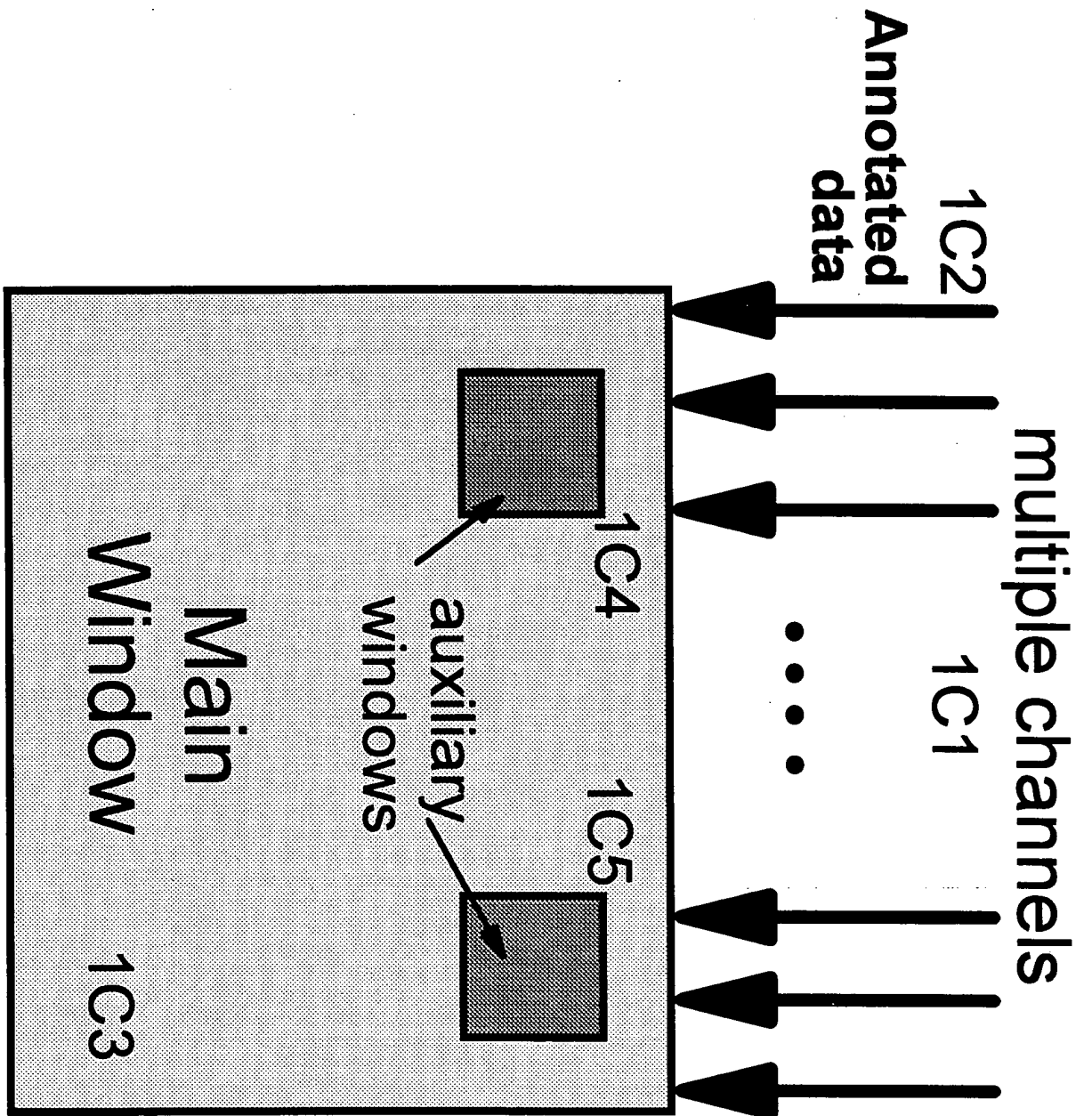


FIG. 1C

[illegible]

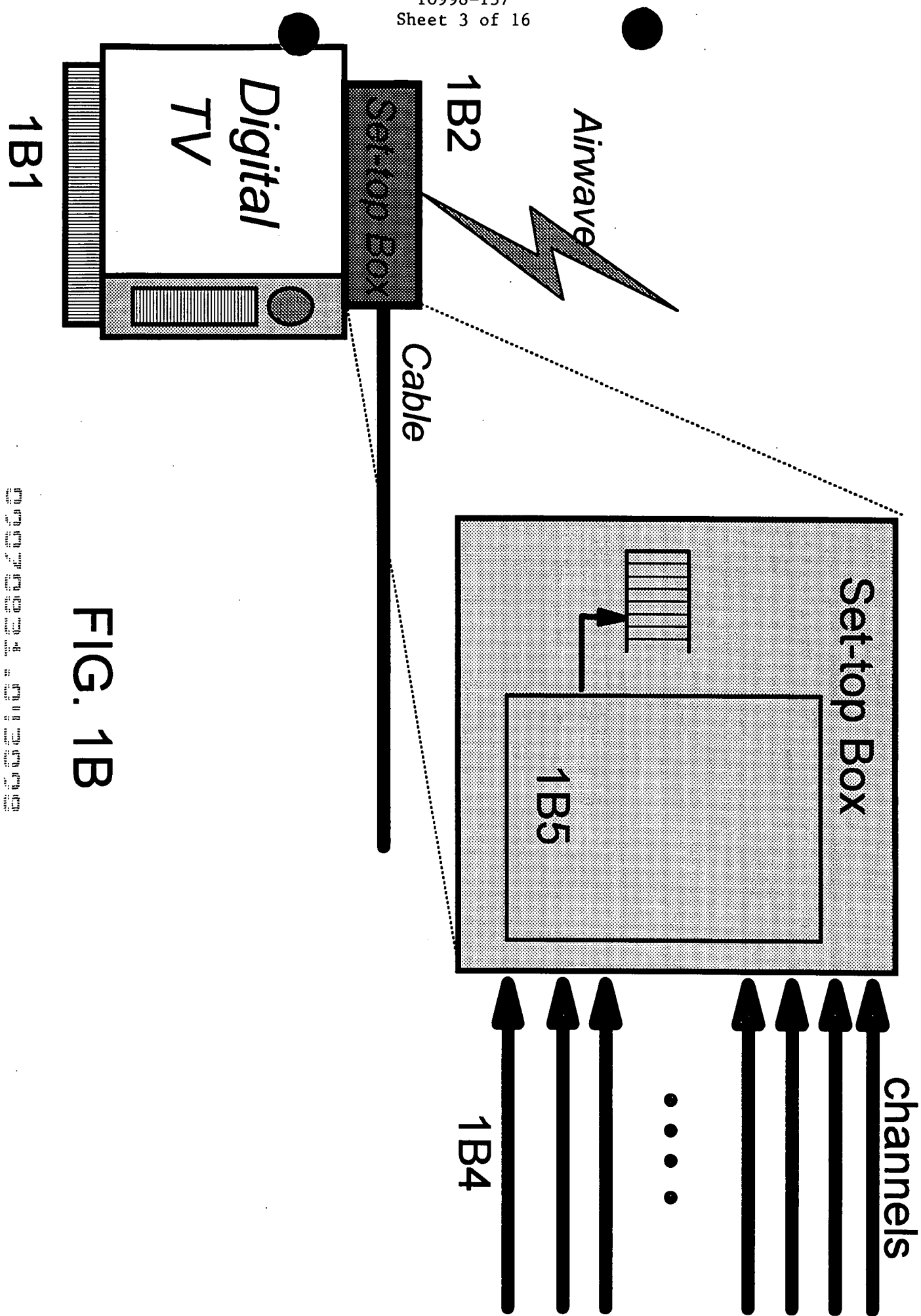


FIG. 1B

FIG. 1B is a block diagram of a digital TV system.

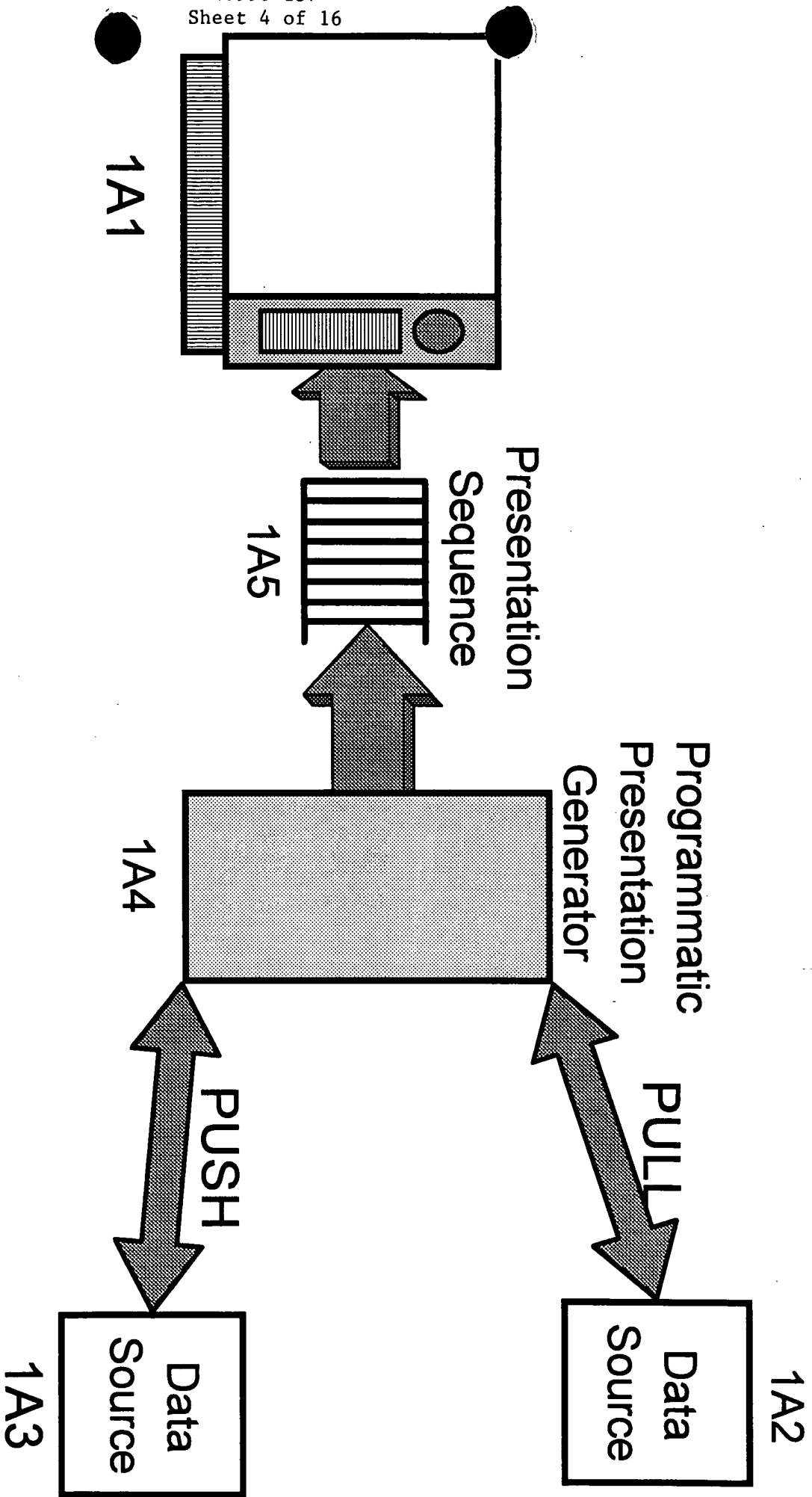


FIG. 1A

FIG. 1A

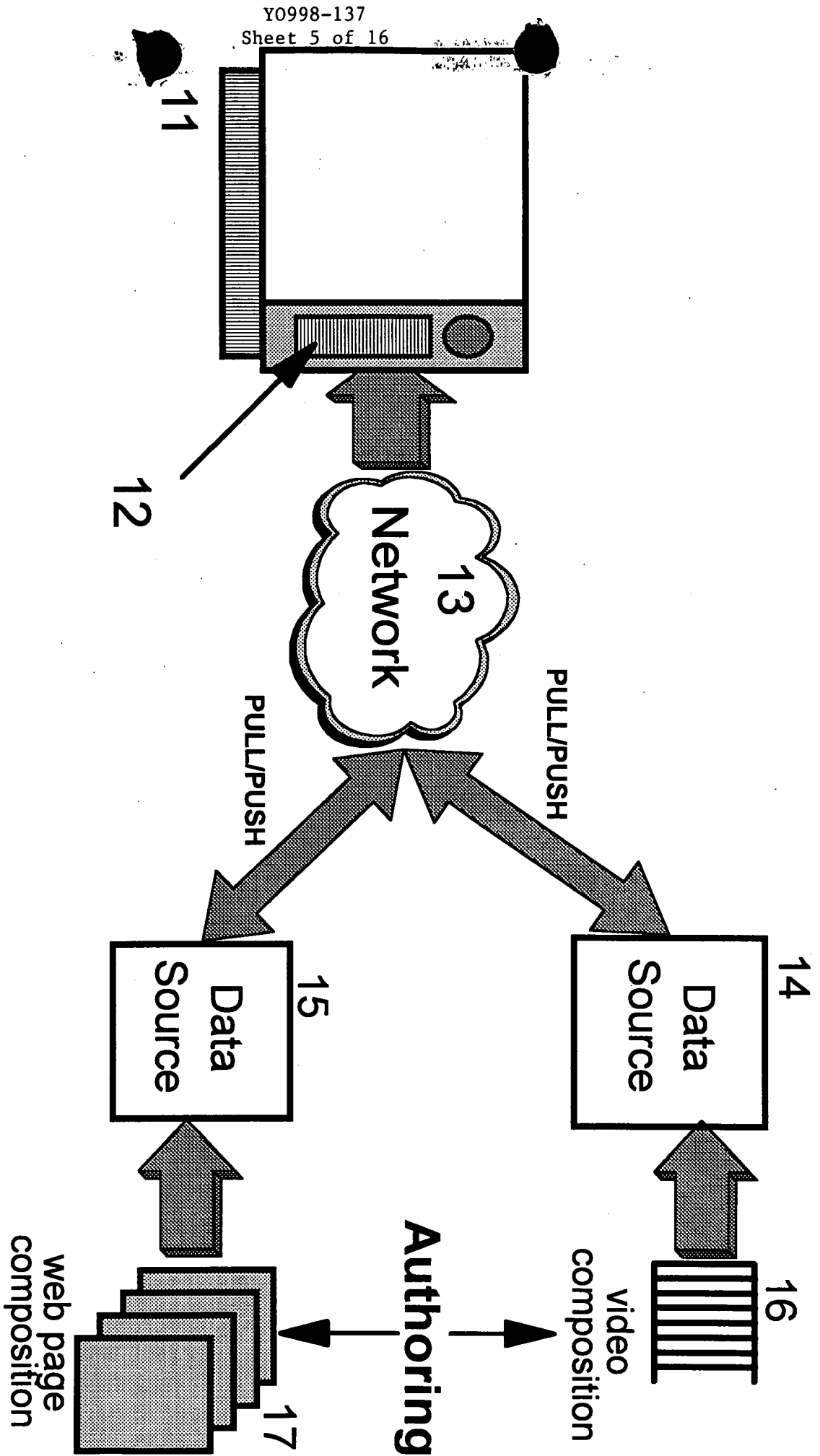


FIG. 1

(PRIOR ART)

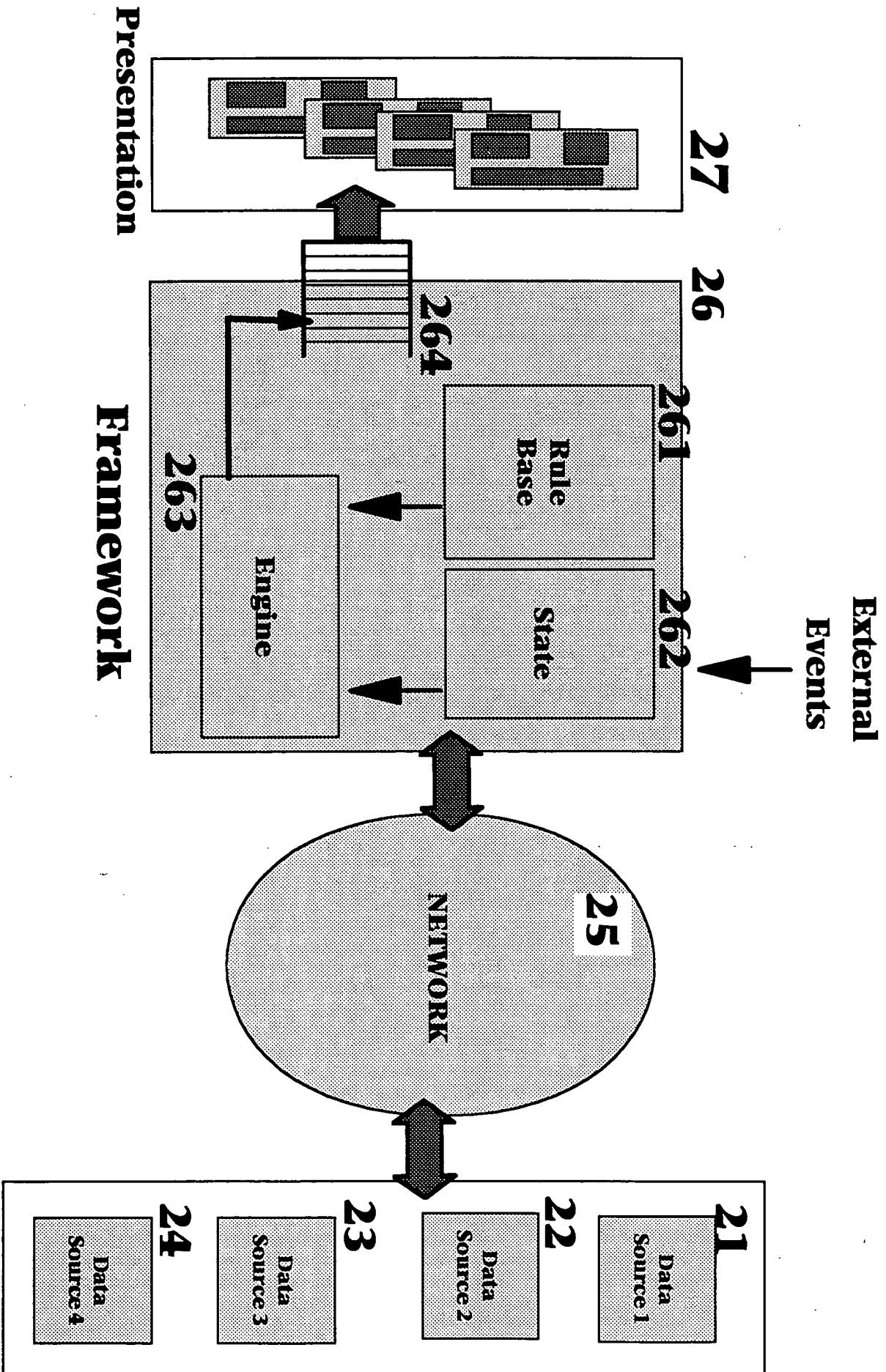


FIG. 2

[illegible]

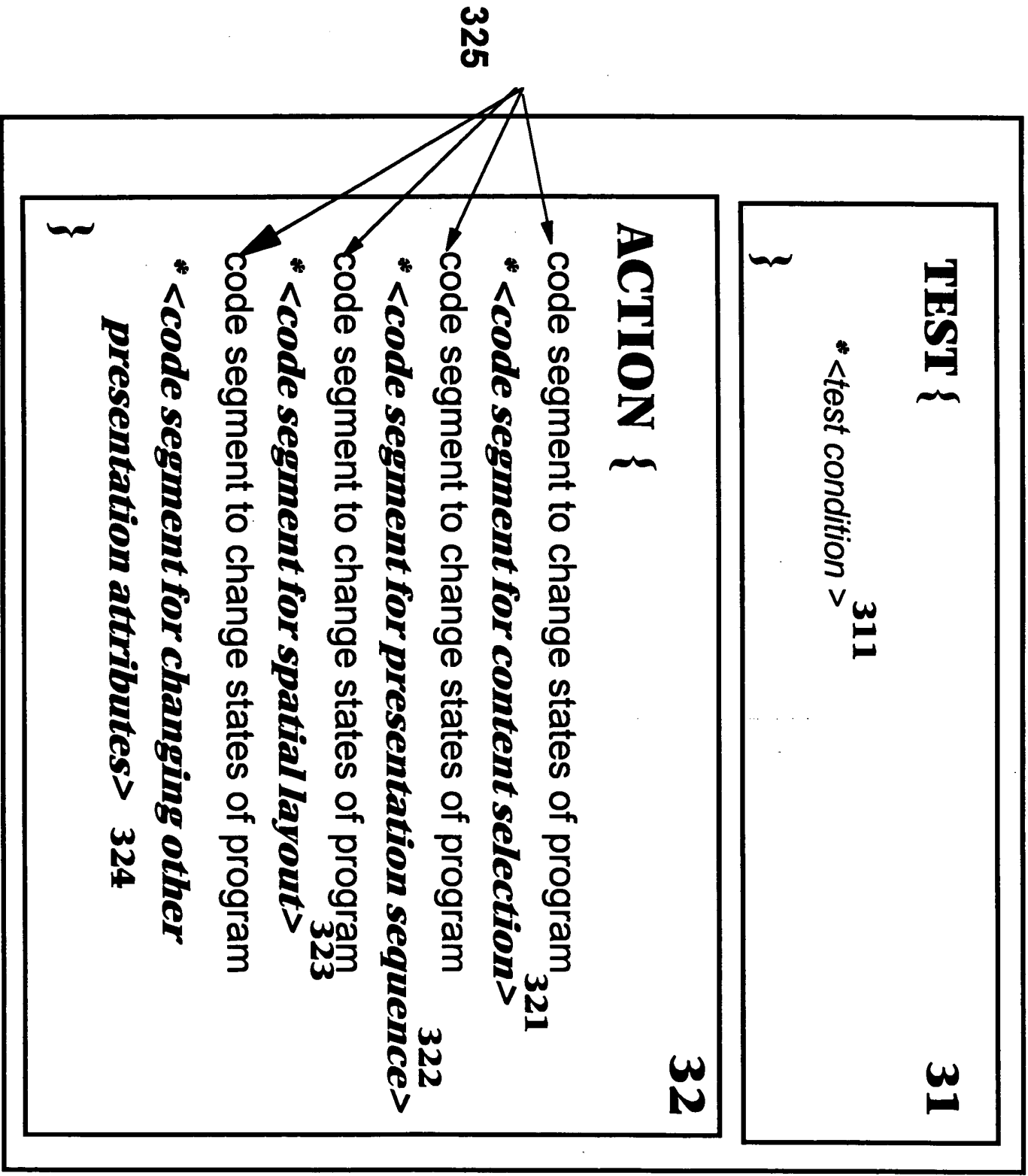


FIG. 3

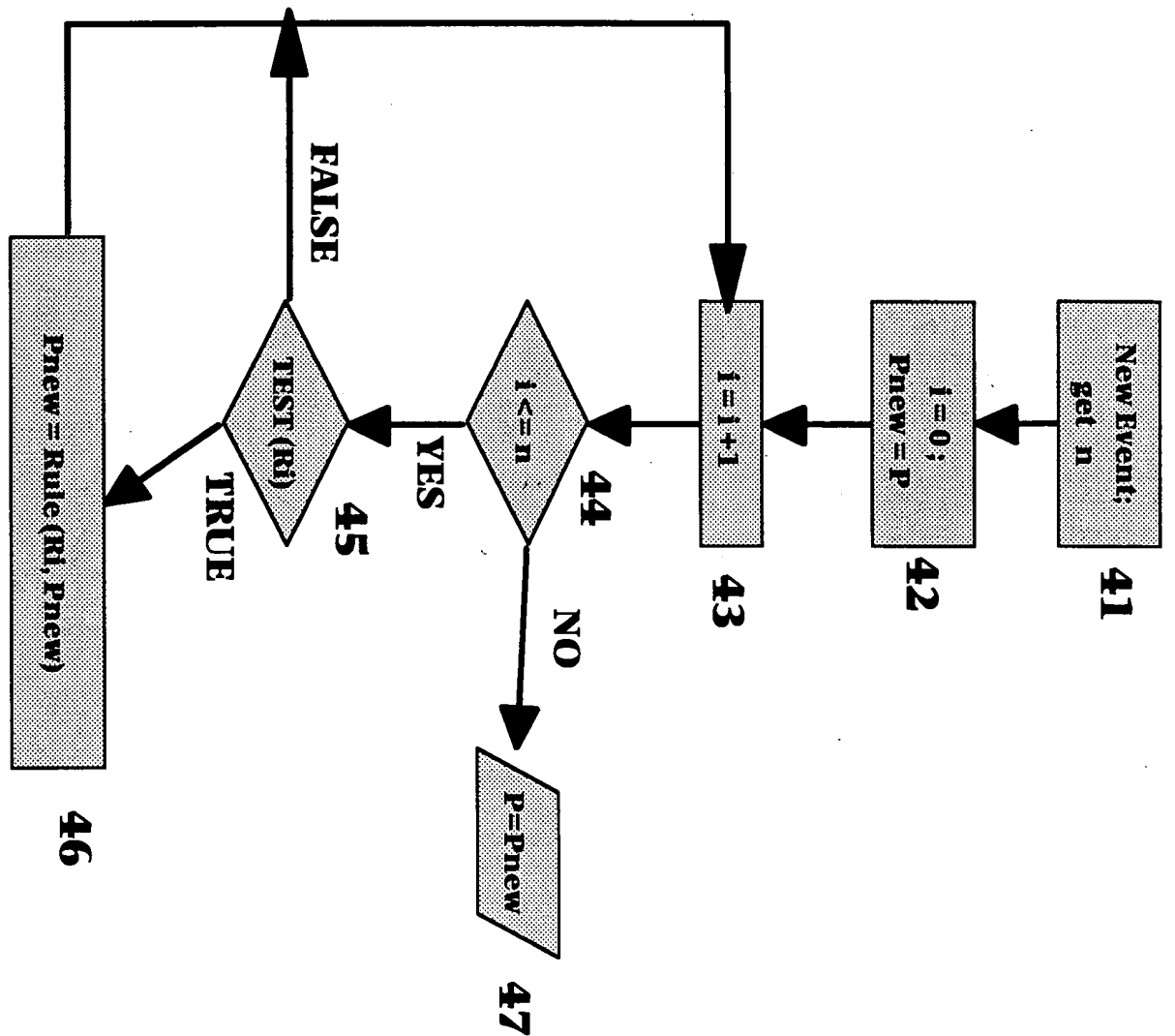


FIG. 4

FIG. 4 is a flowchart of the process for handling new events.

D:

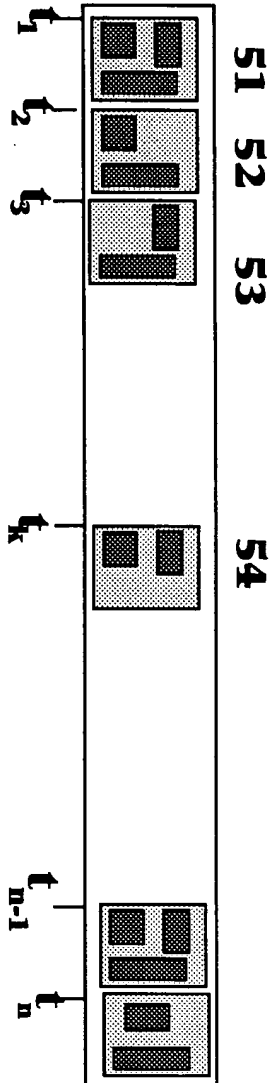


FIG. 5

FIG. 5 is a diagram illustrating a sequence of frames within a rectangular container. The frames are labeled 51, 52, 53, 54, and a final frame. Each frame contains a pattern of black and white squares. The frames are arranged horizontally. Below the frames, time markers t_1 , t_2 , t_3 , t_k , t_{n-1} , and t_n are indicated by arrows pointing to the frames. Frame 51 is at t_1 , frame 52 is at t_2 , frame 53 is at t_3 , frame 54 is at t_k , and the final frame is at t_n . The frame at t_{n-1} is also labeled with t_{n-1} below it.

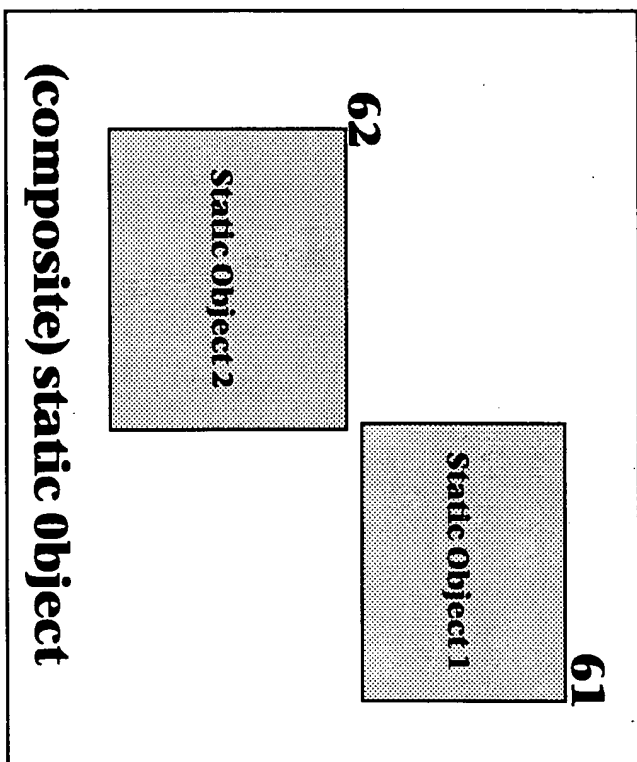


FIG. 6

FIG. 6 is a schematic diagram of a composite static object 61, which includes two static objects 62.

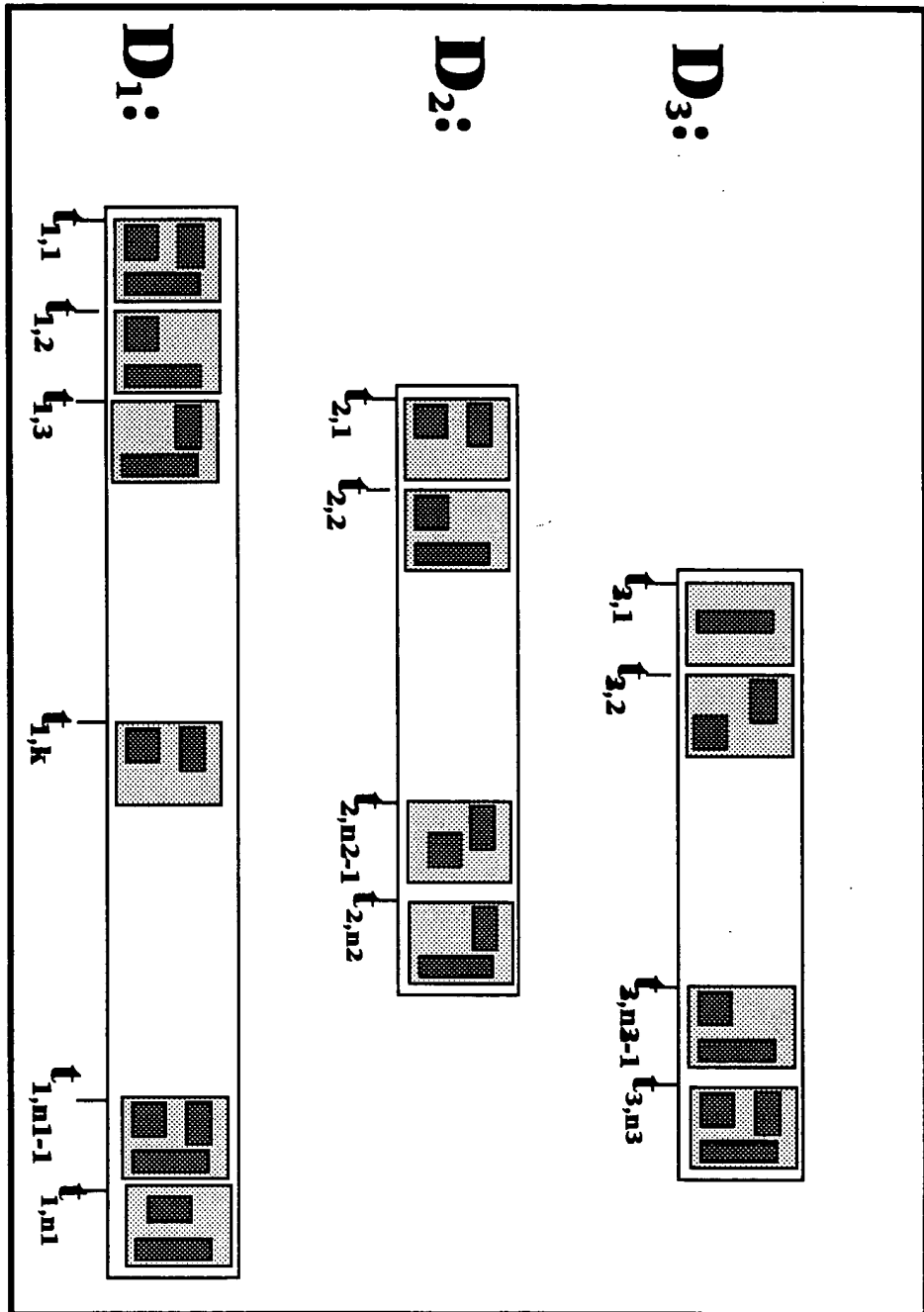
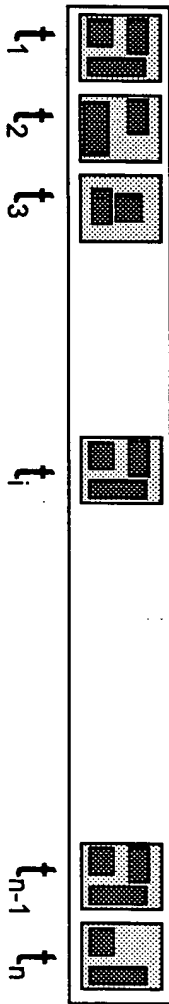
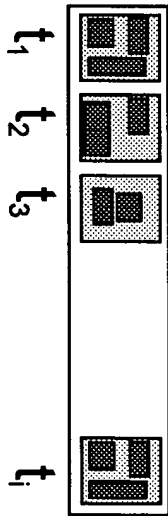


FIG. 7

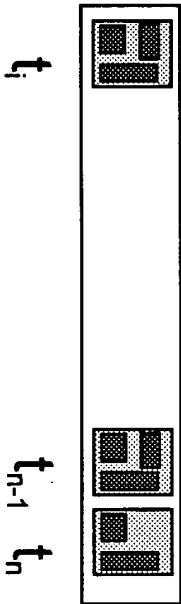
D :



Head (D,i):⁸¹



Tail (D, i):⁸²



Delay (D, t):⁸³

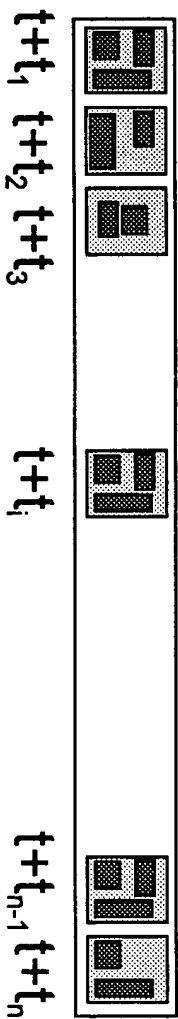


FIG. 8

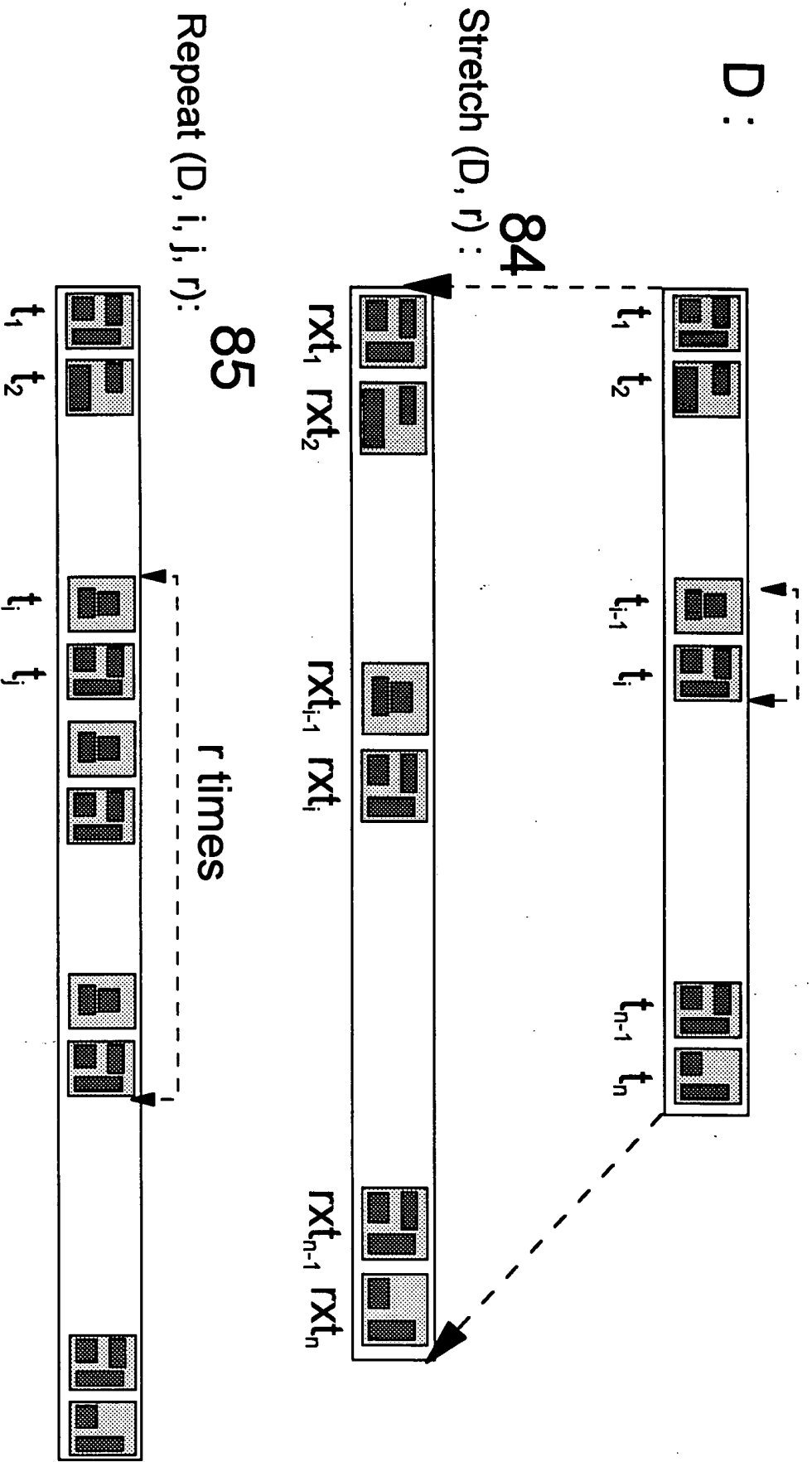


FIG. 8A

FIG. 8A

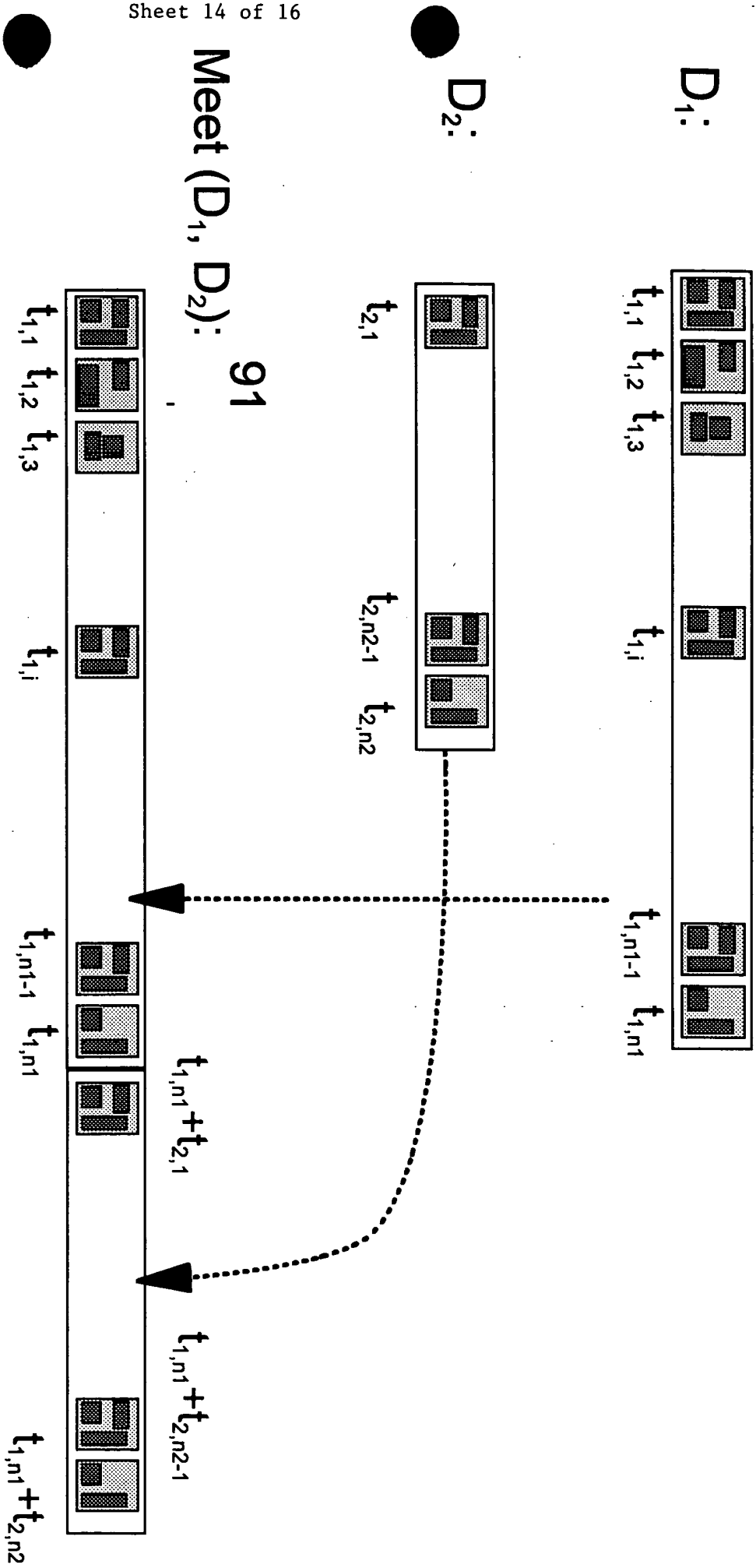
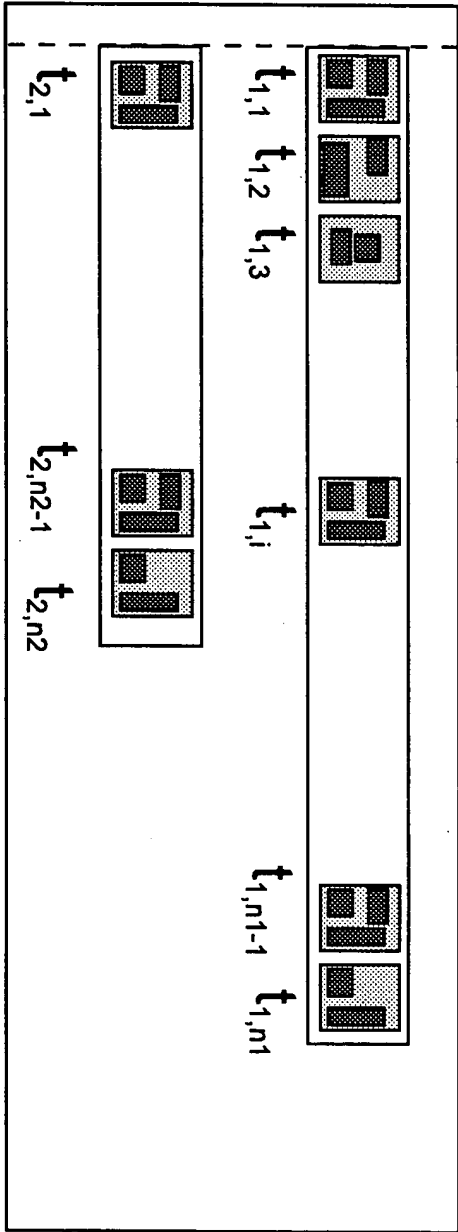


FIG. 9

Co-start(D_1, D_2): 92



Co-end(D_1, D_2): 93

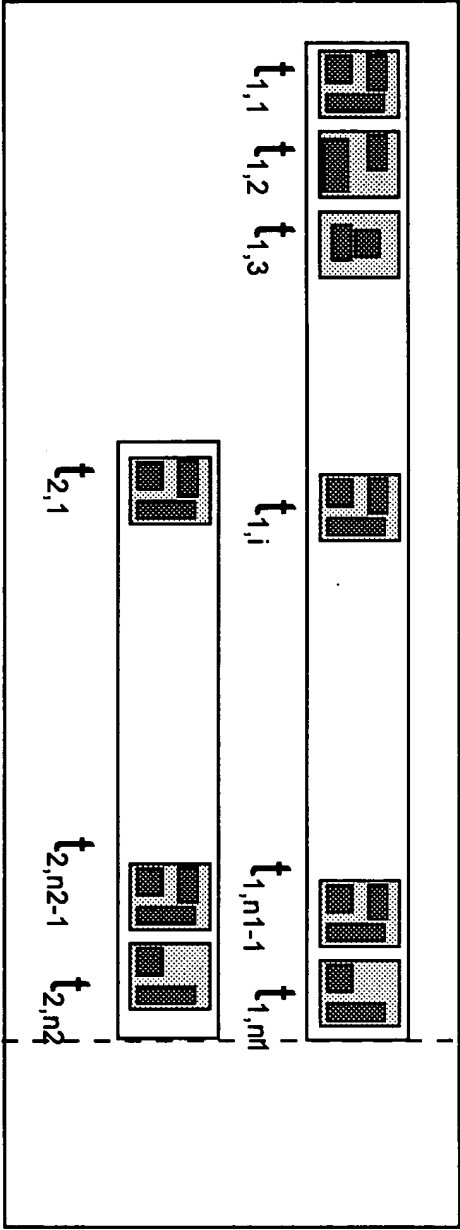
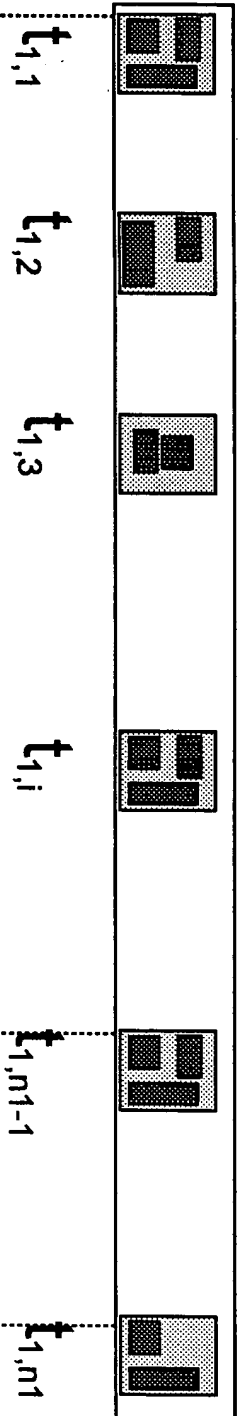
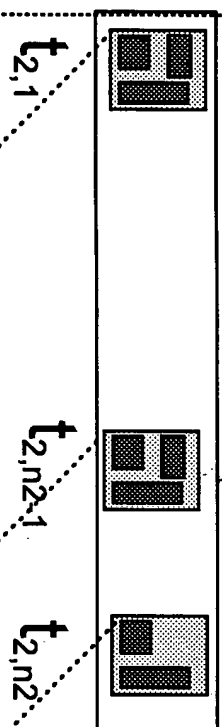


FIG. 9A

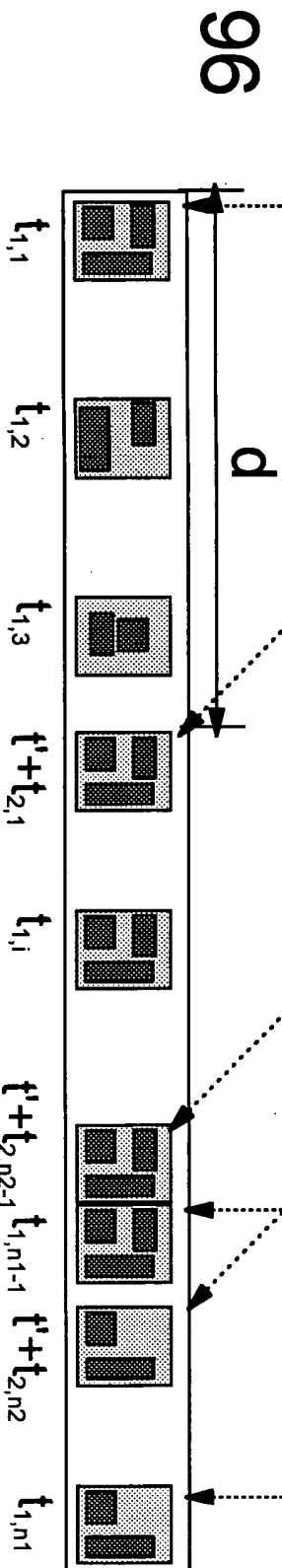
D_1 :



D_2 :



Interleave (D_1, D_2, d):



$$t' = d + t_{1,1}$$

FIG. 9B